Hardware Security Modules

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What? and Why?

• An HSM...

- 1. is a hardware component
- 2. is providing some form of API (fully programmable/key mgmt/etc.)
- 3. actively erases secrets when tampered with
- 4. generally contains a battery and is alwasy-on
- An HSM is not a smartcard

HSM

- Always powered
- Active tamper detection

Smartcard

- Powered off most of the time
- Active tamper detection

Usage scenarios

- CA keys (TLS/code signing)
 - Asymmetric signing keys
- Credit card data
 - Symmetric keys (encryption and authentication)
- Smart meters
 - Asymmetric keys (client certificates)
 - Measurement circuitry

- Misguided attempts at VPN
 - Symmetric keys
- Digital Restriction Management
 - Symmetric keys
- Electronic ID documents
 - Asymmetric signing keys
 - potentially private data sometime in the future

Relevant Standards

- FIPS 140-2 (US govt)
 - US government standard for cryptographic modules
- PCI DSS (PCI SSC)
 - "Payment Card Industry Security Standards Council"
 - Formed by Visa, MasterCard, American Express, Discover, JCB
 - Defines requirements to merchants for processing CC payments
- In both cases: Few concrete criteria, mostly to cert lab

| FEDERAL INFORMATION PROCESSING STANDARDS PUBLICATION (Supercedes FIPS PUB 140-1, 1994 January 11) SECURITY REQUIREMENTS FOR CRYPTOGRAPHIC MODULES | |
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| | |
| Information Technology Laboratory National Institute of Standards and Technology Gaithersburg, MD 20899-8900 | |
| Issued May 25, 2001 | |



U.S. Department of Commerce Donald L. Evans, Secretary

Technology Administration Phillip J. Bond, Under Secretary for Technology

National Institute of Standards and Technology Arden L. Bement, Jr., Director

FIPS 140-2

- US government standard for cryptographic modules
- Four levels, only level 4 is meaningful!
- Active countermeasures, security envelope







- "Payment Card Industry Data Security Standard"
- Enforcement also through fines
- Contains requirements for hard- and software involved in CC data processing
- Most interesting: Requirements to HSMs
- Standard open, but overly vague. Specific requirements are not public.





Commercial products

- Thales, Rohde&Schwarz, IBM, Utimaco,...
- Main form factors: Card terminal, PCI(e) card, 1HU rackmount
- From full CPU access to high-level crypto API
- Processing power in O(smartphone ARM processor)

- Security-by-obscurity (industry favorite!)
- Switches
- Meshes: the only effective technique
- Potting makes meshes more effective

- Light/vibration sensors
- Temperature sensors may be necessary

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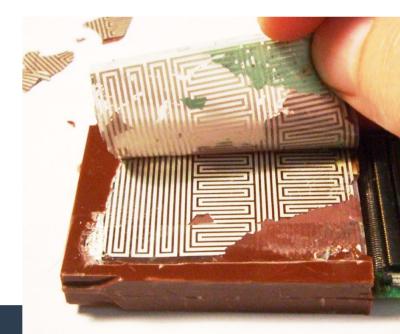
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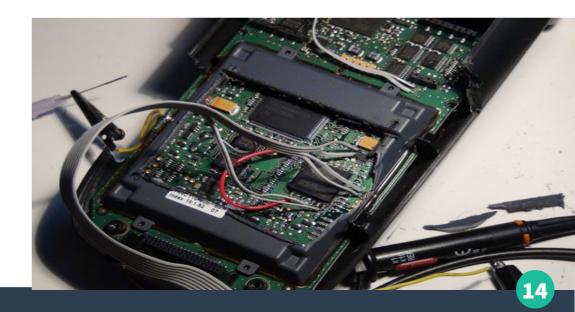
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Practical attacks

- Cold boot, SRAM remanence
 - Turn off, then scrape remains of data out of memory
- Drilling/lasers
 - Mesh at best provides upper bound at size of probe
 - Good meshes: several hundred μm
- Disabling the monitoring circuit
- Bypassing the mesh



Usage scenarios

Good fit

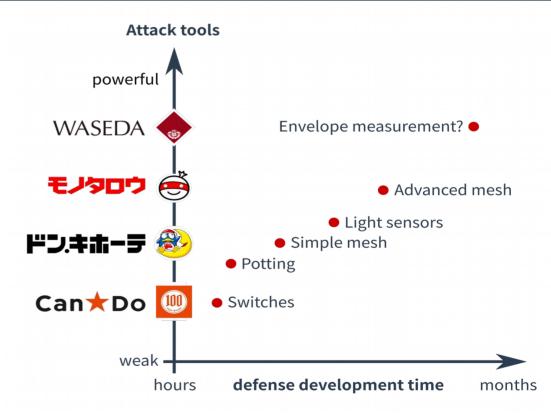
Bad fit

- Instant Messaging encryption
- email encryption & authentication
- Secure Boot/HW root of trust
- → Limited attack budget,
 robust system
 (limited scope of attack)

- Certificate authorities
- DRM
 - → Unbounded attack budget, fragile system (one successful attack suffices)

Take-aways?

- Even a very good HSM only adds to the cost of a oneoff attack 10k US\$ to 100k\$.
- Be careful who you listen to. Lots of wrong information around! (ex.: anything that speaks USB is in general not an HSM!)
- Consider actually **solving the underlying algorithmic problem instead** of using band-aids.
- Designing your own **HSM is not complicated** if you know what to look out for!
- HSMs are only useful in very specific scenarios!



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Research Ideas!

Research directions

• Open source HSM reference design to serve as a research reference standard

- General architecture
- Mesh construction with small-lab resources

• Novel tamper detection techniques

- Acoustic: MEMS/Piezo microphones
- Envelope measurement (Radar/Optics/Ultrasonic acoustics)
- Use triboluminescence for mechanical tamper detection



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Image sources

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